

# SPECIFICATION FOR APPROVAL

( ) Preliminary Specification

(●) Final Specification

Title	47.0" WUXGA TFT LCD
-------	---------------------

BUYER	LGE
MODEL	-

SUPPLIER	LG Display Co., Ltd.
*MODEL	LC470DUE
SUFFIX	SFR1

\*When you obtain standard approval,  
please use the above model name without suffix

APPROVED BY	SIGNATURE	DATE
/		
/		
/		

Please return 1 copy for your confirmation with  
your signature and comments.

APPROVED BY	SIGNATURE	DATE
Y.S.PARK / Team Leader		
REVIEWED BY		
B.Y. KIM/ Project Leader		
PREPARED BY		
T.Y.KIM / Engineer		

TV Product Development Dept.  
LG Display Co., Ltd

## Product Specification

**CONTENTS**

Number	ITEM	Page
	COVER	1
	CONTENTS	2
	RECORD OF REVISIONS	3
1	GENERAL DESCRIPTION	4
2	ABSOLUTE MAXIMUM RATINGS	5
3	ELECTRICAL SPECIFICATIONS	6
3-1	ELECTRICAL CHARACTERISTICS	6
3-2	INTERFACE CONNECTIONS	8
3-3	SIGNAL TIMING SPECIFICATIONS	10
3-4	SIGNAL TIMING WAVEFORMS	11
3-5	COLOR DATA REFERENCE	14
3-6	POWER SEQUENCE	15
4	OPTICAL SPECIFICATIONS	17
5	MECHANICAL CHARACTERISTICS	23
6	RELIABILITY	26
7	INTERNATIONAL STANDARDS	27
7-1	SAFETY	27
7-2	EMC	27
7-3	ENVIRONMENT	27
8	PACKING	28
8-1	DESIGNATION OF LOT MARK	28
8-2	PACKING FORM	28
9	PRECAUTIONS	29
9-1	MOUNTING PRECAUTIONS	29
9-2	OPERATING PRECAUTIONS	29
9-3	ELECTROSTATIC DISCHARGE CONTROL	30
9-4	PRECAUTIONS FOR STRONG LIGHT EXPOSURE	30
9-5	STORAGE	30
9-6	HANDLING PRECAUTIONS FOR PROTECTION FILM	30

## RECORD OF REVISIONS

[illegible]

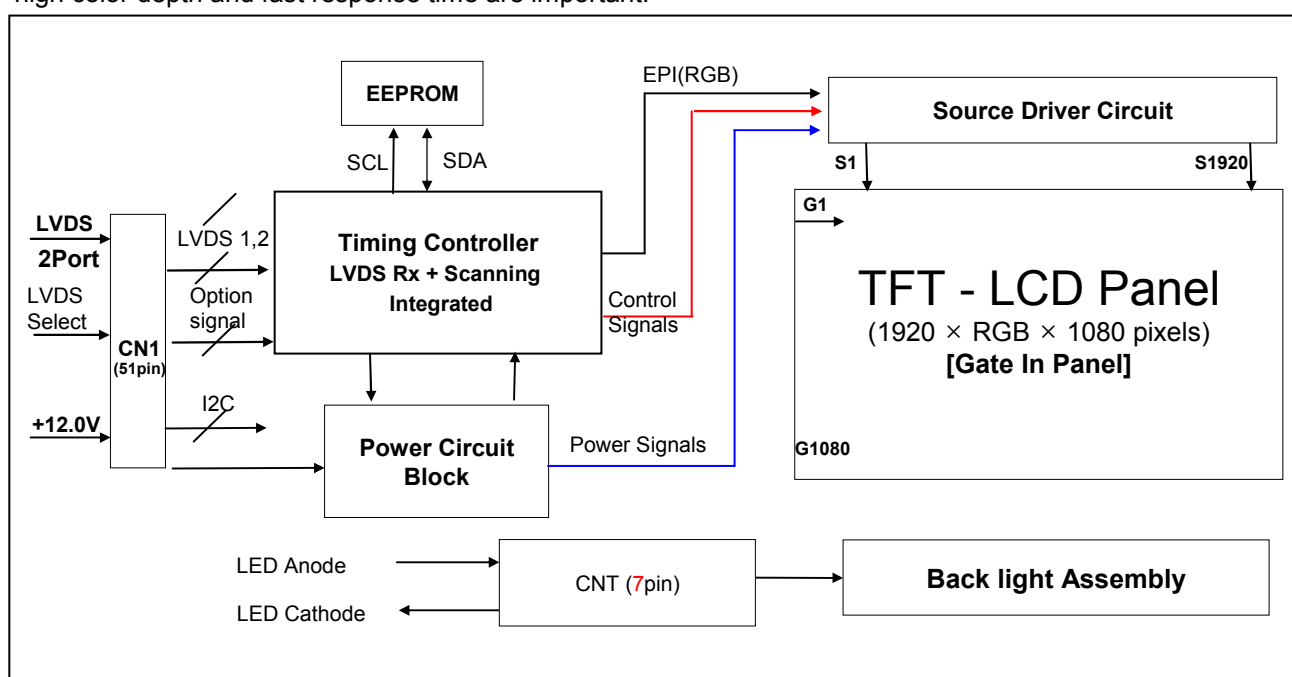
## Product Specification

## 1. General Description

The LC470DUE is a Color Active Matrix Liquid Crystal Display with an integral Light Emitting Diode (LED) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive display type which is operating in the normally black mode. It has a 46.96 inch diagonally measured active display area with WUXGA resolution (1080 vertical by 1920 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arrayed in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 8-bit gray scale signal for each dot. Therefore, it can present a palette of more than 16.7Million colors.

It has been designed to apply the 8-bit 2-port LVDS interface.

It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



## General Features

Active Screen Size	46.96 inches(1192.78mm) diagonal
Outline Dimension	1067.6(H) X 617.4(V) X 36.5(B) mm (Typ.)
Pixel Pitch	0.5415 mm x 0.5415 mm
Pixel Format	1920 horiz. by 1080 vert. Pixels, RGB stripe arrangement
Color Depth	8bit, 16.7Million colors
Luminance, White	300 cd/m <sup>2</sup> (Center 1point ,Typ.)
Viewing Angle (CR>10)	Viewing angle free ( R/L 178 (Min.), U/D 178 (Min.))
Power Consumption	Total 85.92W (Typ.) [Logic= 6.02W, LED only= 79.9W (IF_cathode=400mA)]
Weight	9.2 kg (Typ.)
Display Mode	Transmissive mode, Normally black
Surface Treatment	Hard coating(3H), Anti-glare treatment of the front polarizer (Haze 1%)

## Product Specification

## 2. Absolute Maximum Ratings

The following items are maximum values which, if exceeded, may cause faulty operation or permanent damage to the LCD module.

**Table 1. ABSOLUTE MAXIMUM RATINGS**

Parameter		Symbol	Value		Unit	Note
			Min	Max		
Power Input Voltage	LCD Circuit	V <sub>LCD</sub>	-0.3	+14.0	V <sub>DC</sub>	1
LED Input Voltage	Forward Voltage	V <sub>F</sub>	-	+118	V <sub>DC</sub>	
T-Con Option Selection Voltage		V <sub>LOGIC</sub>	-0.3	+4.0	V <sub>DC</sub>	
Operating Temperature		T <sub>OP</sub>	0	+50	°C	2,3
Storage Temperature		T <sub>ST</sub>	-20	+60	°C	
Panel Front Temperature		T <sub>SUR</sub>	-	+68	°C	4
Operating Ambient Humidity		H <sub>OP</sub>	10	90	%RH	2,3
Storage Humidity		H <sub>ST</sub>	10	90	%RH	

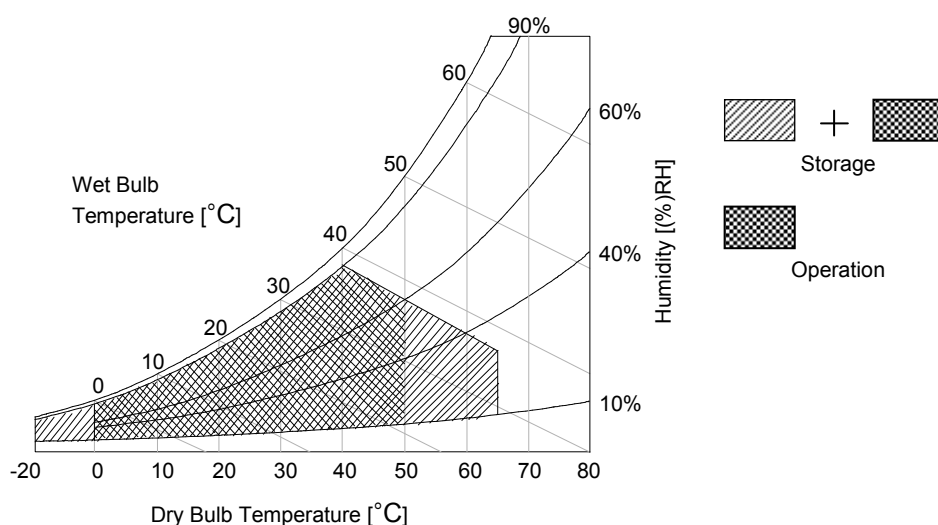
Note1. Ambient temperature condition ( $T_a = 25 \pm 2 \text{ }^{\circ}\text{C}$ )

2. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be Max 39°C, and no condensation of water.

3. Gravity mura can be guaranteed below 40°C condition.

4. The maximum operating temperatures is based on the test condition that the surface temperature of display area is less than or equal to 68°C with LCD module alone in a temperature controlled chamber. Thermal management should be considered in final product design to prevent the surface temperature of display area from being over 68°C. The range of operating temperature may be degraded in case of improper thermal management in final product design.



## Product Specification

### 3. Electrical Specifications

#### 3-1. Electrical Characteristics

It requires two power inputs. One is employed to power for the LCD circuit. The other is used for the LED backlight and LED Driver circuit.

**Table 2. ELECTRICAL CHARACTERISTICS**

Parameter	Symbol	Value			Unit	Note
		Min	Typ	Max		
Circuit :						
Power Input Voltage	VLCD	10.8	12.0	13.2	VDC	
Power Input Current	ILCD	-	501	626	mA	1
		-	717	897	mA	2
Power Consumption	PLCD		6.02	8.26	Watt	1
Rush current	IRUSH	-	-	5.0	A	3

Note 1. The specified current and power consumption are under the  $V_{LCD}=12.0V$ ,  $T_a=25 \pm 2^\circ C$ ,  $f_v=60Hz$  condition, and mosaic pattern(8 x 6) is displayed and  $f_v$  is the frame frequency.

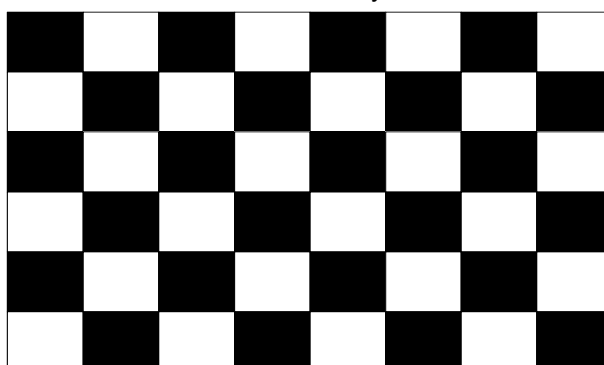
2. The current is specified at the maximum current pattern.

3. The duration of rush current is about 2ms and rising time of power input is 0.5ms (min.).

4. Ripple voltage level is recommended under  $\pm 5\%$  of typical voltage

White : 255 Gray

Black : 0 Gray



**Mosaic Pattern(8 x 6)**

## Product Specification

Table 3. ELECTRICAL CHARACTERISTICS (Continue)

Parameter		Symbol	Values			Unit	Note
			Min	Typ	Max		
Backlight Assembly :							
Forward Current (one array)	Anode	I <sub>F (anode)</sub>		400		mAdc	±5%
	Cathode	I <sub>F (cathode)</sub>		400		mAdc	2, 3
Forward Voltage		V <sub>F</sub>	91.8	99.9	108	Vdc	4
Power Consumption		P <sub>BL</sub>	-	79.9	86.4	W	6
Burst Dimming Duty		On duty	1		100	%	
Burst Dimming Frequency		1/T	95		182	Hz	8
LED Array : (APPENDIX-V)							
Life Time			30,000			Hrs	7

Notes :The design of the LED driver must have specifications for the LED array in LCD Assembly.

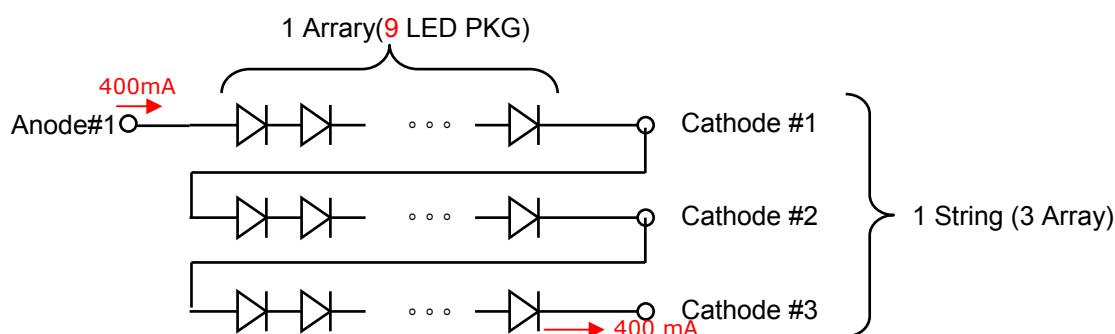
The electrical characteristics of LED driver are based on Constant Current driving type.

The performance of the LED in LCM, for example life time or brightness, is extremely influenced by the characteristics of the LED Driver. So, all the parameters of an LED driver should be carefully designed.

When you design or order the LED driver, please make sure unwanted lighting caused by the mismatch of the LED and the driver (no lighting, flicker, etc) has never been occurred. When you confirm it, the LCD–Assembly should be operated in the same condition as installed in your instrument.

1. Electrical characteristics are based on LED Array specification.
2. Specified values are defined for a Backlight Assembly. (IBL :6 LED array/LCM)
3. Each LED array has **one** anode terminal and one cathode terminals.

The forward current( $I_F$ ) of the anode terminal is **400mA** and it supplies **400mA** into **1** strings, respectively



4. The forward voltage( $V_F$ ) of LED array depends on ambient temperature (Appendix-V)
5. Maximum level of power consumption is measured at initial turn on.  
Typical level of power consumption is measured after 1hrs aging at  $25 \pm 2^\circ\text{C}$ .
6. The life time(MTTF) is determined as the time at which brightness of the LED is 50% compared to that of initial value at the typical LED current on condition of continuous operating at  $25 \pm 2^\circ\text{C}$ , based on duty 100%.
7. The reference method of burst dimming duty ratio.  
It is recommended to use synchronous V-sync frequency to prevent waterfall  
( $V_{\text{sync}} * 2 = \text{Burst Frequency}$ )  
Though PWM frequency is over 182Hz (max252Hz), function of backlight is not affected.

## Product Specification

**3-2. Interface Connections**

This LCD module employs two kinds of interface connection, 51-pin connector is used for the module electronics and 14-pin connector is used for the integral backlight system.

**3-2-1. LCD Module**

-LCD Connector(CN1): FI-RE51S-HF(manufactured by JAE)

- Mating Connector : FI-R51HL(JAE) or compatible

**Table 4. MODULE CONNECTOR(CN1) PIN CONFIGURATION**

No	Symbol	Description	No	Symbol	Description
1	NC or GND	No Connection or Ground	27	NC	No Connection
2	NC	No Connection (Note 4)	28	R2AN	SECOND LVDS Receiver Signal (A-)
3	NC	No Connection (Note 4)	29	R2AP	SECOND LVDS Receiver Signal (A+)
4	NC	No Connection (Note 4)	30	R2BN	SECOND LVDS Receiver Signal (B-)
5	NC	No Connection (Note 4)	31	R2BP	SECOND LVDS Receiver Signal (B+)
6	NC	No Connection (Note 4)	32	R2CN	SECOND LVDS Receiver Signal (C-)
7	LVDS Select	'H' =JEIDA , 'L' or NC = VESA	33	R2CP	SECOND LVDS Receiver Signal (C+)
8	NC	No Connection (Note 4)	34	GND	Ground
9	NC	No Connection (Note 4)	35	R2CLKN	SECOND LVDS Receiver Clock Signal(-)
10	NC	No Connection (Note 4)	36	R2CLKP	SECOND LVDS Receiver Clock Signal(+)
11	GND	Ground	37	GND	Ground
12	R1AN	FIRST LVDS Receiver Signal (A-)	38	R2DN	SECOND LVDS Receiver Signal (D-)
13	R1AP	FIRST LVDS Receiver Signal (A+)	39	R2DP	SECOND LVDS Receiver Signal (D+)
14	R1BN	FIRST LVDS Receiver Signal (B-)	40	NC	No Connection
15	R1BP	FIRST LVDS Receiver Signal (B+)	41	NC	No Connection
16	R1CN	FIRST LVDS Receiver Signal (C-)	42	NC or GND	No Connection or Ground
17	R1CP	FIRST LVDS Receiver Signal (C+)	43	NC or GND	No Connection or Ground
18	GND	Ground	44	GND	Ground
19	R1CLKN	FIRST LVDS Receiver Clock Signal(-)	45	GND	Ground
20	R1CLKP	FIRST LVDS Receiver Clock Signal(+)	46	GND	Ground
21	GND	Ground	47	NC	No connection
22	R1DN	FIRST LVDS Receiver Signal (D-)	48	VLCD	Power Supply +12.0V
23	R1DP	FIRST LVDS Receiver Signal (D+)	49	VLCD	Power Supply +12.0V
24	NC	No Connection	50	VLCD	Power Supply +12.0V
25	NC	No Connection	51	VLCD	Power Supply +12.0V
26	NC or GND	No Connection or Ground	-	-	-

- Note 1. All GND(ground) pins should be connected together to the LCD module's metal frame.  
 2. All VLCD (power input) pins should be connected together.  
 3. All Input levels of LVDS signals are based on the **EIA 644** Standard.  
 4. #1~#6 & #8~#10 NC (No Connection): These pins are used only for LGD (Do not connect)  
 5. Specific pin No. **#44** is used for "No signal detection" of system signal interface.  
 It should be GND for NSB(No Signal Black) during the system interface signal is not.  
 If this pin is "H", LCD Module displays AGP(Auto Generation Pattern).



Product Specification

3-2-2. Backlight Module

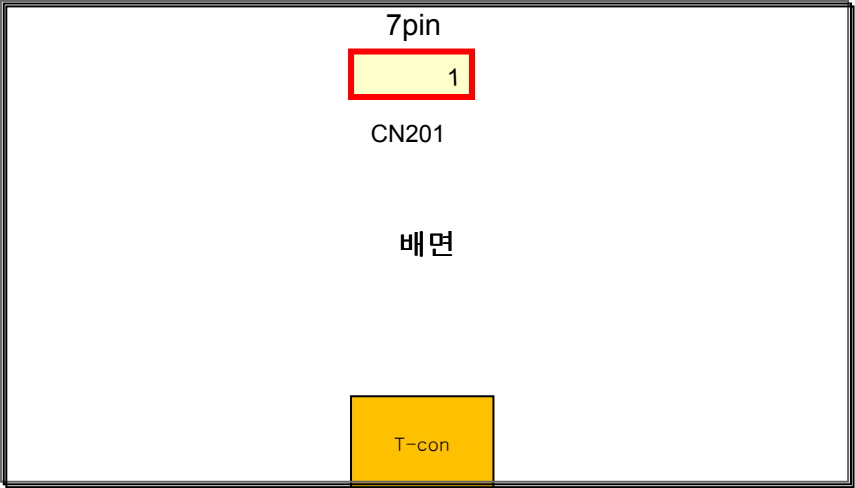
[ CN201 ]

- 1) LED Array assy Connector (Plug)  
: SMH200-07
- 2) Mating Connector (Receptacle)  
: SMAW200A-H07AA2

Table 5. BACKLIGHT CONNECTOR PIN CONFIGURATION(CN201)

No	Symbol(CN201)	Description	Note
1	L2 Cathode	LED Output Current L2	
2	NC		
3	L2 Anode	LED Input Current for L2	
4	NC		
5	L1 Cathode	LED Output Current for L1	
6	NC		
7	L1 Anode	LED Input Current for L1	

◆ Rear view of LCM



LED 1Bar	1 Block
LED 2Bar	
LED 3Bar	2 Block
LED 4Bar	
LED 5Bar	3 Block
LED 6Bar	

## Product Specification

**3-3. Signal Timing Specifications**

Table 6 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timings should be satisfied with the following specification for normal operation.

**Table 6. TIMING TABLE (DE Only Mode)**

ITEM		Symbol	Min	Typ	Max	Unit	Note
Horizontal	Display Period	t <sub>HV</sub>	960	960	960	tCLK	1920 / 2
	Blank	t <sub>HB</sub>	100	140	240	tCLK	1
	Total	t <sub>HP</sub>	1060	1100	1200	tCLK	
Vertical	Display Period	t <sub>VV</sub>	1080	1080	1080	Lines	
	Blank	t <sub>VB</sub>	20 (228)	45 (270)	69 (300)	Lines	1
	Total	t <sub>VP</sub>	1100 (1308)	1125 (1350)	1149 (1380)	Lines	

ITEM		Symbol	Min	Typ	Max	Unit	Note
Frequency	DCLK	f <sub>CLK</sub>	63.00	74.25	78.00	MHz	
	Horizontal	f <sub>H</sub>	57.3	67.5	70	KHz	2
	Vertical	f <sub>V</sub>	57 (47)	60 (50)	63 (53)	Hz	2 NTSC (PAL)

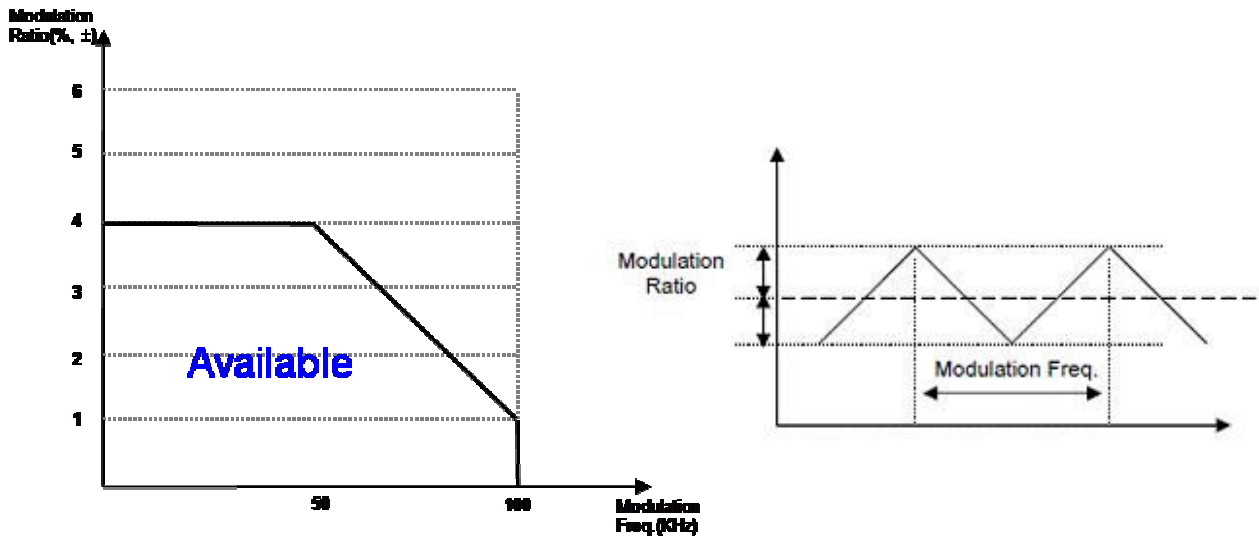
notes: 1. The input of HSYNC & VSYNC signal does not have an effect on normal operation (DE Only Mode).  
If you use spread spectrum of EMI, add some additional clock to minimum value for clock margin.

2. The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate and the horizontal frequency

3. Spread Spectrum Rate (SSR) for 50KHz ~ 100kHz Modulation Frequency(FMOD) is calculated by  $(7 - 0.06 \cdot F_{mod})$ , where Modulation Frequency (FMOD) unit is KHz.  
LVDS Receiver Spread spectrum Clock is defined as below figure

※ Timing should be set based on clock frequency.

## Product Specification

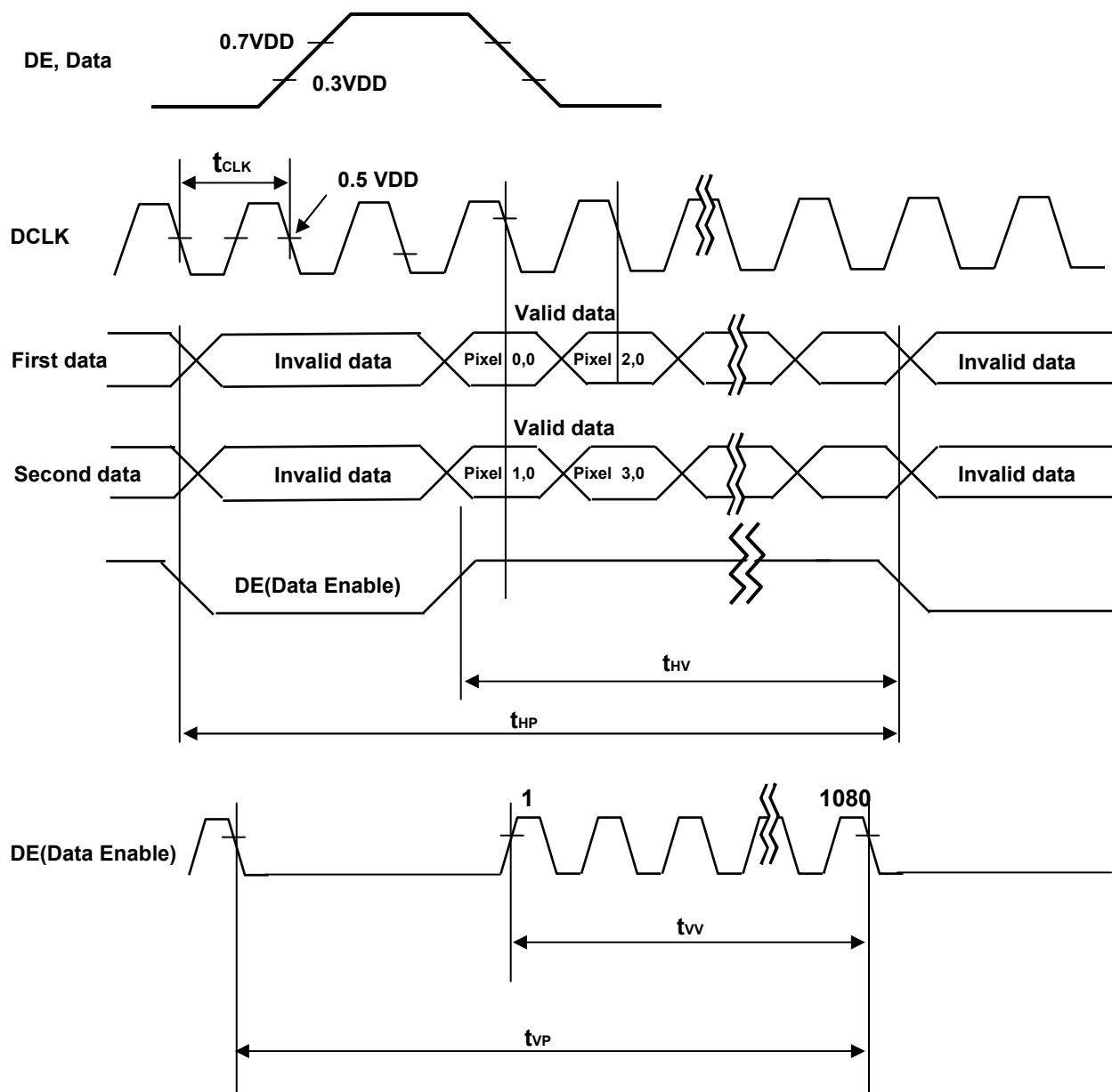


※ Please pay attention to the followings when you set Spread Spectrum Rate(SSR) and Modulation Frequency(FMOD)

1. Please set proper Spread Spectrum Rate(SSR) and Modulation Frequency (FMOD) of TV system LVDS output.
2. Please check FOS after you set Spread Spectrum Rate(SSR) and Modulation Frequency(FMOD) to avoid abnormal display. Especially, harmonic noise can appear when you use Spread Spectrum under FMOD 30 KHz.

### 3-4. LVDS Signal Specification

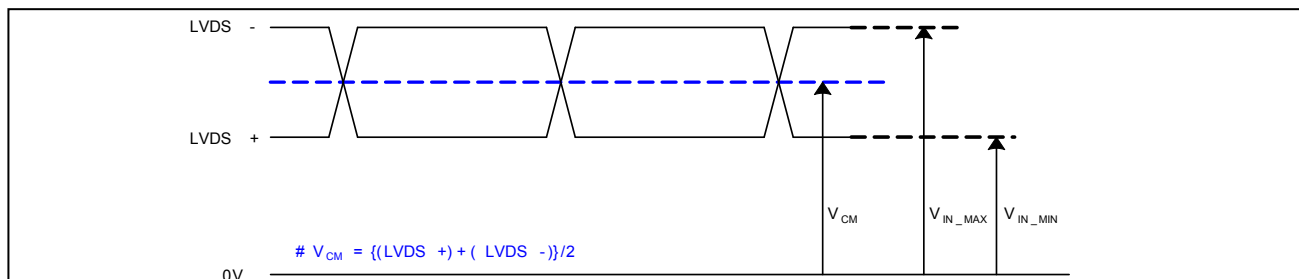
#### 3-4-1. LVDS Input Signal Timing Diagram



## Product Specification

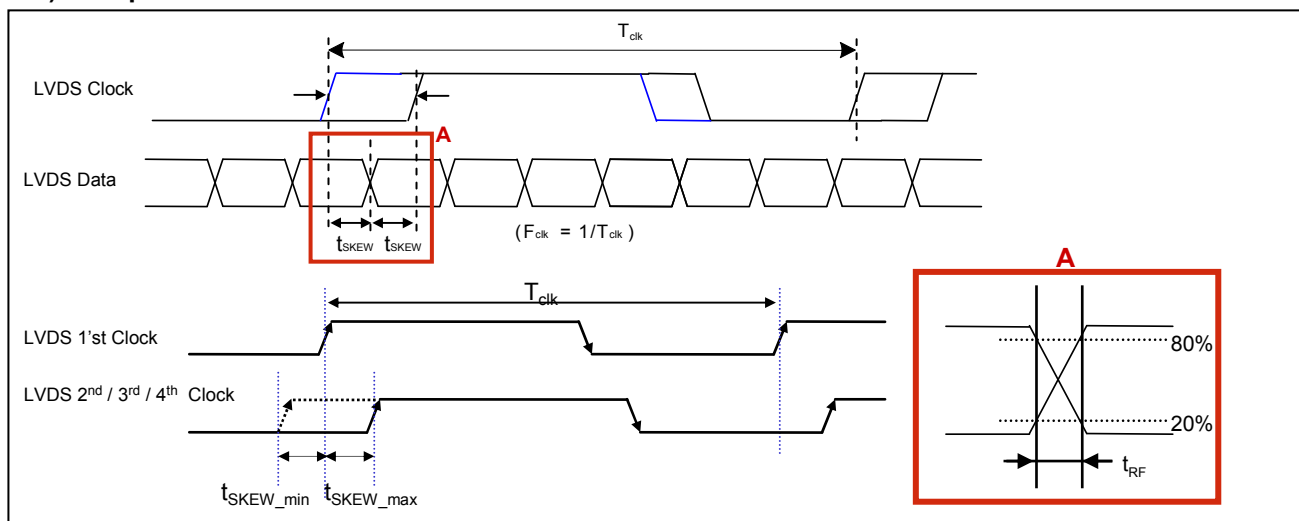
## 3-4-2. LVDS Input Signal Characteristics

## 1) DC Specification



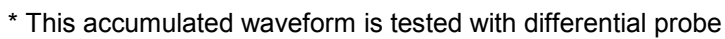
Description	Symbol	Min	Max	Unit	Note
LVDS Common mode Voltage	$V_{CM}$	1.0	1.5	V	-
LVDS Input Voltage Range	$V_{IN}$	0.7	1.8	V	-
Change in common mode Voltage	$\Delta V_{CM}$	-	250	mV	-

## 2) AC Specification



Description	Symbol	Min	Max	Unit	Note
LVDS Differential Voltage	High Threshold $V_{TH}$	100	300	mV	3
	Low Threshold $V_{TL}$	-300	-100	mV	
LVDS Clock to Data Skew	$t_{SKEW}$	-	$  (0.25 * T_{clk}) / 7  $	ps	-
LVDS Clock/DATA Rising/Falling time	$t_{RF}$	260	$  (0.3 * T_{clk}) / 7  $	ps	2
Effective time of LVDS	$t_{eff}$	$  \pm 360  $	-	ps	-
LVDS Clock to Clock Skew (Even to Odd)	$t_{SKEW\_EO}$	-	$  1/7 * T_{clk}  $	ps	-

- Note
1. All Input levels of LVDS signals are based on the EIA 644 Standard.
  2. If  $t_{RF}$  isn't enough,  $t_{eff}$  should be meet the range.
  3. LVDS Differential Voltage is defined within  $t_{eff}$



## Product Specification

## 3-5. Color Data Reference

The brightness of each primary color(red,green,blue) is based on the 8bit gray scale data input for the color. The higher binary input, the brighter the color. Table 7 provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

Color		Input Color Data																								
		RED								GREEN								BLUE								
		MSB				LSB				MSB				LSB				MSB				LSB				
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0	
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Green (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	
	Blue (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
RED	RED (000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	RED (001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	...	...								...								...								
	RED (254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	RED (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
GREEN	GREEN (000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	GREEN (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	
	...	...								...								...								
	GREEN (254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	
	GREEN (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	
BLUE	BLUE (000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	BLUE (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	
	...	...								...								...								
	BLUE (254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	
	BLUE (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	

## Product Specification

## 3-6. Power Sequence

## 3-6-1. LCD Driving circuit

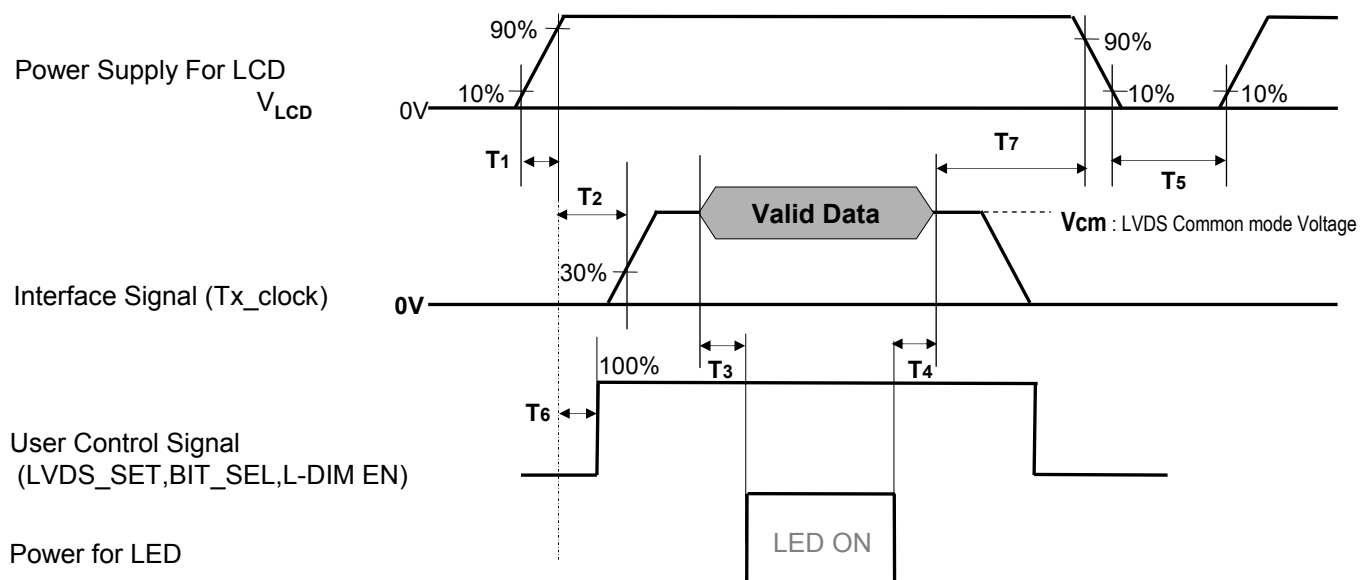


Table 8. POWER SEQUENCE

Parameter	Value			Unit	Notes
	Min	Typ	Max		
T1	0.5	-	20	ms	1
T2	0	-	-	ms	2
T3	400	-	-	ms	3
T4	200	-	-	ms	3
T5	1.0	-	-	s	4
T6	0	-	T2	ms	5
T7	0	-	-	s	6

Note :

1. Even though T1 is over the specified value, there is no problem if I2T spec of fuse is satisfied.
  2. If T2 is satisfied with specification after removing LVDS Cable, there is no problem.
  3. The T3 / T4 is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.
  4. T5 should be measured after the Module has been fully discharged between power off and on period.
  5. If the on time of signals (Interface signal and user control signals) precedes the on time of Power ( $V_{LCD}$ ), it will be happened abnormal display. When T6 is NC status, T6 doesn't need to be measured.
  6. If there is no abnormal display, no problem.
  7. It is recommendation specification that T8 has to be 100ms as a minimum value.
- ※ Please avoid floating state of interface signal at invalid period.  
 ※ When the power supply for LCD (VLCD) is off, be sure to pull down the valid and invalid data to 0V.



## Product Specification

#### 4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable in a dark environment at  $25 \pm 2^\circ\text{C}$ . The values are specified at 50cm from the LCD surface at a viewing angle of  $\Phi$  and  $\theta$  equal to  $0^\circ$ .

FIG. 1 shows additional information concerning the measurement equipment and method.

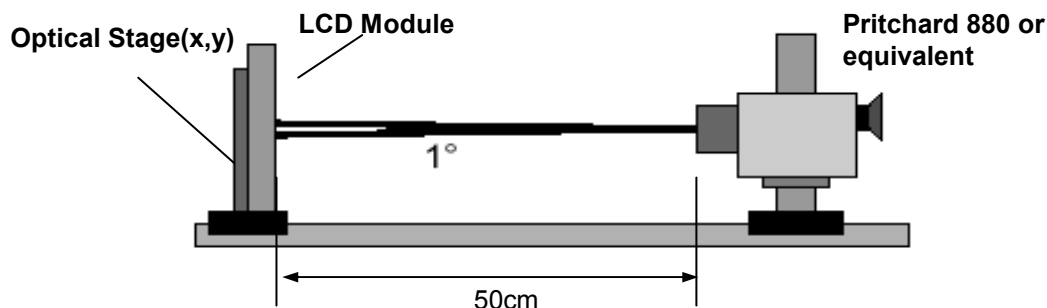


FIG. 1 Optical Characteristic Measurement Equipment and Method

$T_a = 25 \pm 2^\circ\text{C}$ ,  $V_{\text{LCD}} = 12.0\text{V}$ ,  $f_v = 60\text{Hz}$ ,  $D_{\text{clk}} = 74.25\text{MHz}$ ,

$I_{\text{F\_cathode}} = 400\text{mA (Typ)}$

Table 10. OPTICAL CHARACTERISTICS

Parameter			Symbol		Value			Unit	Note
					Min	Typ	Max		
Contrast Ratio			CR		1000	1200	-		1
Surface Luminance, white			L <sub>WH</sub>	2D	240	300	-	cd/m <sup>2</sup>	2
				3D	90	110			8
Luminance Variation			δ <sub>WHITE</sub>	9P	60	70	-	%	3
Response Time	G to G (BW)		G to G BW		-	9	13		4
Color Coordinates [CIE1931]	RED		R <sub>x</sub>	Typ -0.03	0.650	Typ +0.03			
			R <sub>y</sub>		0.333				
	GREEN	G <sub>x</sub>	0.303						
		G <sub>y</sub>	0.590						
	BLUE	B <sub>x</sub>	0.150						
		B <sub>y</sub>	0.063						
	WHITE	W <sub>x</sub>	0.281						
		W <sub>y</sub>	0.288						
Color Temperature						10,000		K	
Color Gamut						68		%	
Viewing Angle	2D (CR>10)	x axis, right(φ=0°)	θ <sub>r</sub>	89	-	-	degree	5	
		x axis, left (φ=180°)	θ <sub>l</sub>	89	-	-			
		y axis, up (φ=90°)	θ <sub>u</sub>	89	-	-			
		y axis, down (φ=270°)	θ <sub>d</sub>	89	-	-			
Gray Scale					-	-	-		6

### Product Specification

Note : 1. Contrast Ratio(CR) is defined mathematically as :

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$

It is measured at center 1-point.

2. Surface luminance are determined after the unit has been 'ON' and 1 Hour after lighting the backlight in a dark environment at  $25 \pm 2^\circ\text{C}$ . Surface luminance is the luminance value at center 1-point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see the FIG. 2.
3. The variation in surface luminance,  $\delta$  WHITE is defined as :  
 $\delta \text{ WHITE}(9P) = \text{Minimum}(L_{on1}, L_{on2}, \dots, L_{on8}, L_{on9}) / \text{Maximum}(L_{on1}, L_{on2}, \dots, L_{on8}, L_{on9}) \times 100(\%)$   
 Where  $L_{on1}$  to  $L_{on9}$  are the luminance with all pixels displaying white at 9 locations .  
 For more information, see the FIG. 2.
4. Response time is the time required for the display to transit from any gray to white (Rise Time,  $Tr_R$ ) and from any gray to black (Decay time,  $Tr_D$ ). For additional information see the FIG. 3.  
 ※ G to  $G_{BW}$  Spec stands for average value of all measured points.  
 Photo Detector : RD-80S / Field :  $2^\circ$
5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 4.
6. Gray scale specification  
 Gamma Value is approximately 2.2. For more information, see the Table 11.

**Table 11. GRAY SCALE SPECIFICATION**

Gray Level	Luminance [%] (Typ)
L0	-
L15	0.27
L31	1.04
L47	2.49
L63	4.68
L79	7.66
L95	11.5
L111	16.1
L127	21.6
L143	28.1
L159	35.4
L175	43.7
L191	53
L207	63.2
L223	74.5
L239	86.7
L255	100

## Product Specification

Measuring point for surface luminance & measuring point for luminance variation.

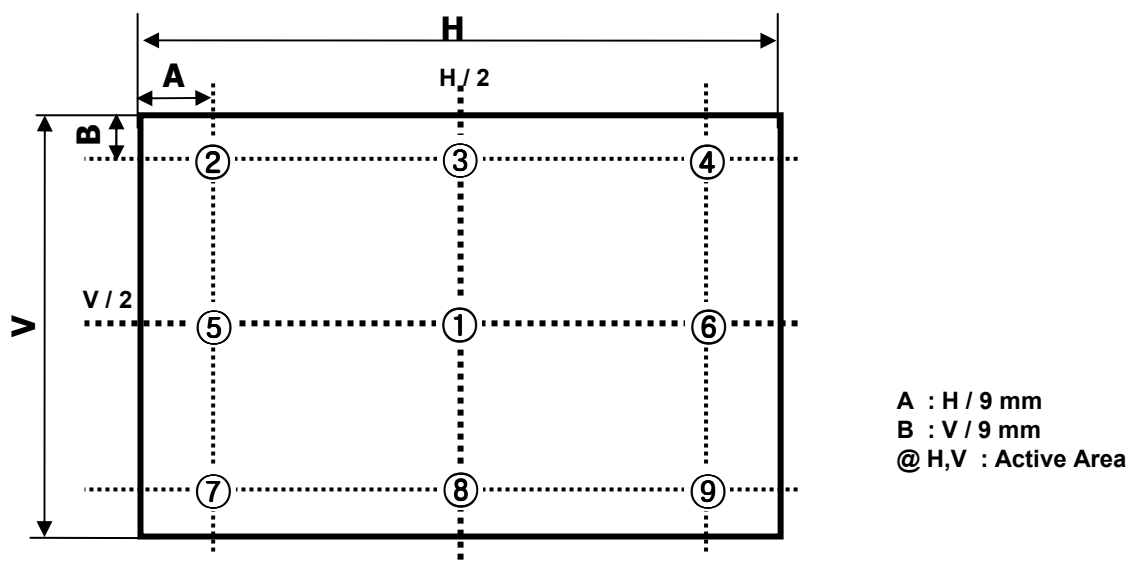


FIG. 2 9 Points for Luminance Measure

Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Gray(M)".

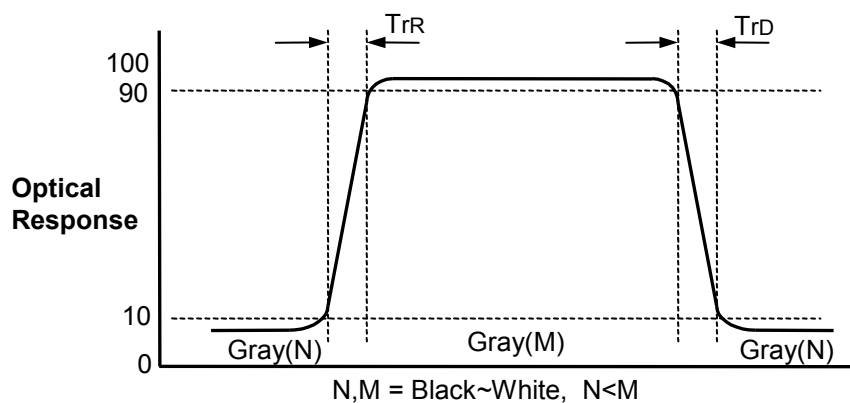


FIG. 3 Response Time

## Product Specification

Dimension of viewing angle range

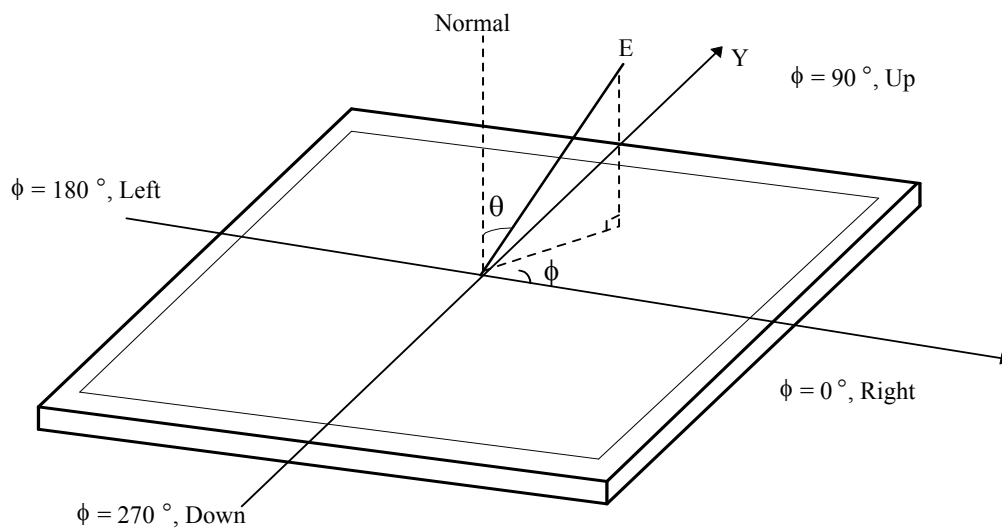


FIG. 4 Viewing Angle

Measuring point for Contrast Ratio

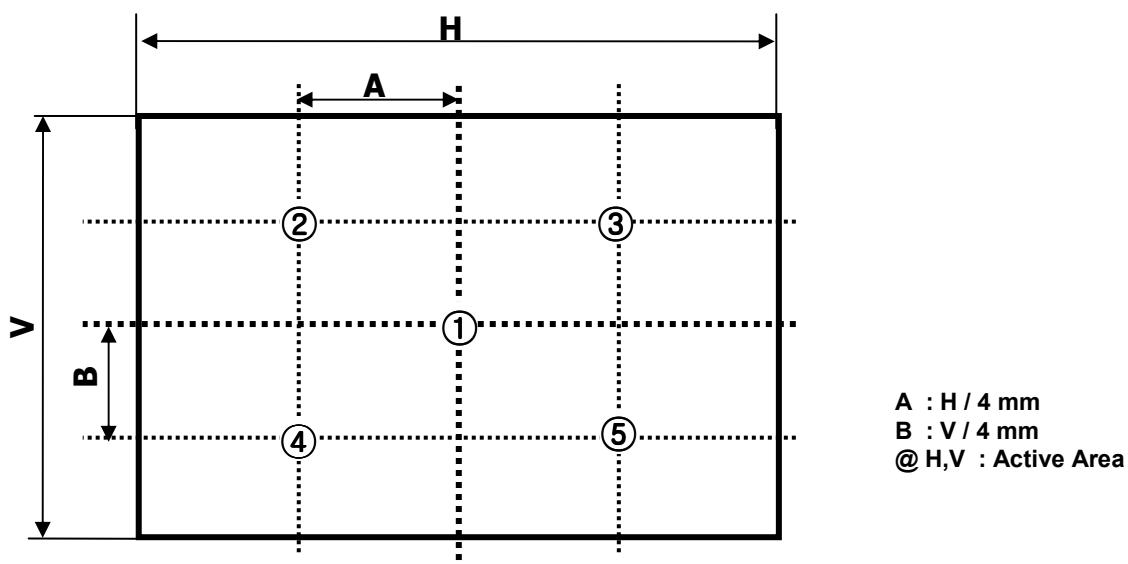


FIG. 5 5 Points for Contrast Ratio Measure

## Product Specification

**5. Mechanical Characteristics**

Table 12 provides general mechanical characteristics.

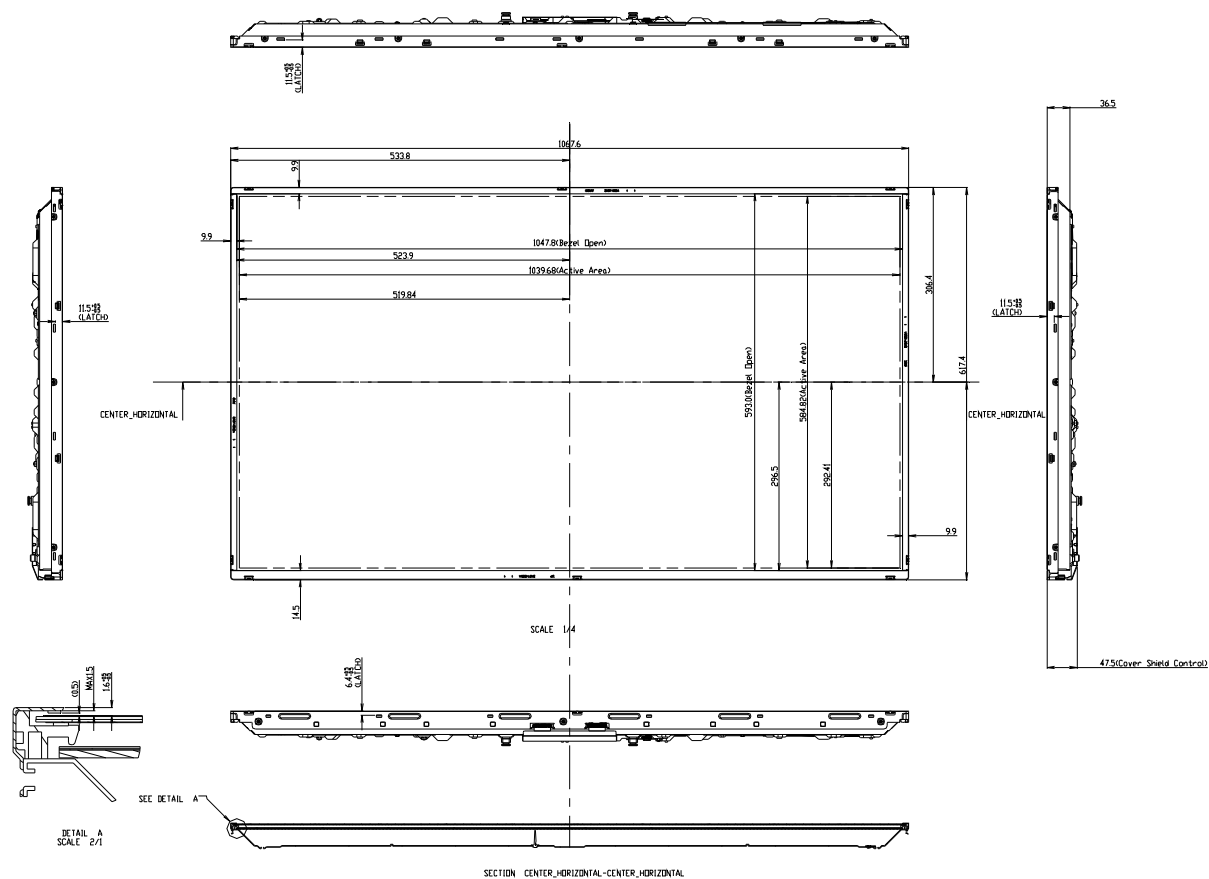
**Table 12. MECHANICAL CHARACTERISTICS**

Item	Value	
Outline Dimension	Horizontal	1067.6 mm
	Vertical	617.4 mm
	Depth	36.5 mm
Bezel Area	Horizontal	1047.8 mm
	Vertical	593.0 mm
Active Display Area	Horizontal	1039.68 mm
	Vertical	584.82 mm
Weight	9.2 Kg (Typ.), 10.2 kg (Max.)	

Note : Please refer to a mechanical drawing in terms of tolerance at the next page.

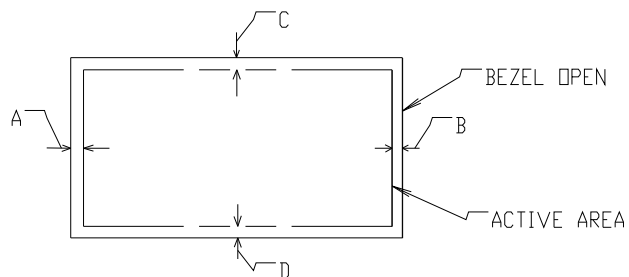
## Product Specification

## [ FRONT VIEW ]



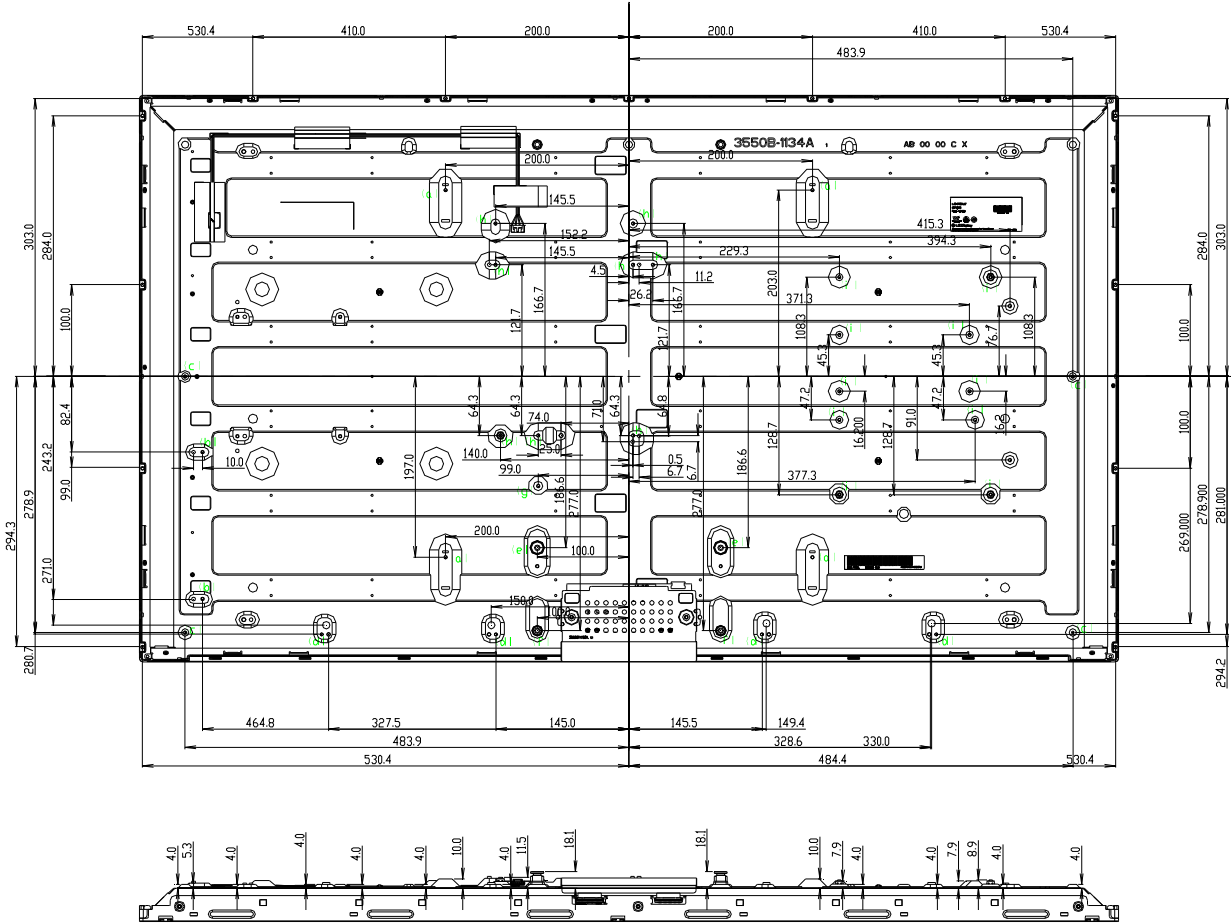
## NOTE

1. Unspecified tolerances are to be  $\pm 1.0\text{mm}$ .
2. Tilt and partial disposition tolerance of display area is as following.
  - (1) X-direction :  $1A-B1 < 1.6\text{mm}$
  - (2) Y-direction :  $1C-D1 < 1.6\text{mm}$
3. Max. torque : refer to table.
4. Max. depth : refer to table.
5. The same feature means the same dimension.



Product Specification

[ REAR VIEW ]



ITEM	UDM HIGHT (mm)	TAP	MAX. DEPTH (mm)	TOURGUE (kgf.cm)	NOTE
(a)	10.0	M3	9.0	Max. 8.0 (kgf.cm)	4ea
(b)	5.3	M3	4.7		2ea
(c)	4.0	M3	3.4		4ea
(d)	4.0	M3	3.4		4ea
(e)	18.1	M4	8.5		2ea
(f)	14.0	M4	8.5		2ea
(g)	6.0	M3	5.4		1ea
(h)	10.5	M3	9.9		8ea
(i)	7.9	M3	7.3		10ea
(j)	3.9	M3	3.3		2ea
(k)	10.5	M4	9.9		1ea

## Product Specification

## 6. Reliability

**Table 13. ENVIRONMENT TEST CONDITION**

No.	Test Item	Condition
1	High temperature storage test	Ta= 60°C 240h
2	Low temperature storage test	Ta= -20°C 240h
3	High temperature operation test	Ta= 50°C 50%RH 240h
4	Low temperature operation test	Ta= 0°C 240h
5	Vibration test (non-operating)	Wave form : random Vibration level : 1.0Grms Bandwidth : 10-300Hz Duration : X,Y,Z, Each direction per 10 min
6	Shock test (non-operating)	Shock level : 50Grms Waveform : half sine wave, 11ms Direction : $\pm X$ , $\pm Y$ , $\pm Z$ One time each direction
7	Humidity condition Operation	Ta= 40 °C ,90%RH
8	Altitude operating storage / shipment	0 - 16,400 ft 0 - 40,000 ft

Note : Before and after Reliability test, LCM should be operated with normal function.



## Product Specification

## 7. International Standards

### 7-1. Safety

- a) UL 60065, Underwriters Laboratories Inc.  
Audio, Video and Similar Electronic Apparatus - Safety Requirements.
- b) CAN/CSA C22.2 No.60065:03, Canadian Standards Association.  
Audio, Video and Similar Electronic Apparatus - Safety Requirements.
- c) EN 60065, European Committee for Electrotechnical Standardization (CENELEC).  
Audio, Video and Similar Electronic Apparatus - Safety Requirements.
- d) IEC 60065, The International Electrotechnical Commission (IEC).  
Audio, Video and Similar Electronic Apparatus - Safety Requirements.  
(Including report of IEC60825-1:2001 clause 8 and clause 9)

#### Notes

##### 1. Laser (LED Backlight) Information

Class 1M LED Product  
IEC60825-1 : 2001  
Embedded LED Power (Class1M)

##### 2. Caution

: LED inside.

Class 1M laser (LEDs) radiation when open.

Do not open while operating.

### 7-2. EMC

- a) ANSI C63.4 "American National Standard for Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the Range of 9 kHz to 40 GHz." American National Standards Institute (ANSI), 2003.
- b) CISPR 22 "Information technology equipment – Radio disturbance characteristics – Limit and methods of measurement." International Special Committee on Radio Interference (CISPR), 2005.
- c) CISPR 13 "Sound and television broadcast receivers and associated equipment – Radio disturbance characteristics – Limits and method of measurement." International Special Committee on Radio Interference (CISPR), 2006.

### 7-3. Environment

- a) RoHS, Directive 2002/95/EC of the European Parliament and of the council of 27 January 2003

## Product Specification

**8. Packing****8-1. Information of LCM Label**

## a) Lot Mark

A	B	C	D	E	F	G	H	I	J	K	L	M
---	---	---	---	---	---	---	---	---	---	---	---	---

A,B,C : SIZE(INCH)  
E : MONTH

D : YEAR  
F ~ M : SERIAL NO.

## Note

## 1. YEAR

Year	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020
Mark	A	B	C	D	E	F	G	H	J	K

## 2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	A	B	C

## b) Location of Lot Mark

Serial NO. is printed on the label. The label is attached to the backside of the LCD module.  
This is subject to change without prior notice.

**8-2. Packing Form**

a) Package quantity in one Pallet : 18 pcs

b) Pallet Size : 1140 mm(W) X 990 mm(D) X 625 mm(H)

## 9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

### 9-1. Mounting Precautions

- (1) You must mount a module using specified mounting holes (Details refer to the drawings).
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaked with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

### 9-2. Operating Precautions

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage :  
 $V = \pm 200\text{mV}$  (Over and under shoot voltage)
- (2) Response time depends on the temperature. (In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)  
 And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw.  
 (if not, it can cause conductive particles and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.
- (10) The conductive material and signal cables are kept away from LED driver inductor to prevent abnormal display, sound noise and temperature rising.

### 9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

### 9-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

### 9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.  
It is recommended that they be stored in the container in which they were shipped.
- (3) Storage condition is guaranteed under packing conditions.
- (4) The phase transition of Liquid Crystal in the condition of the low or high storage temperature will be recovered when the LCD module returns to the normal condition.

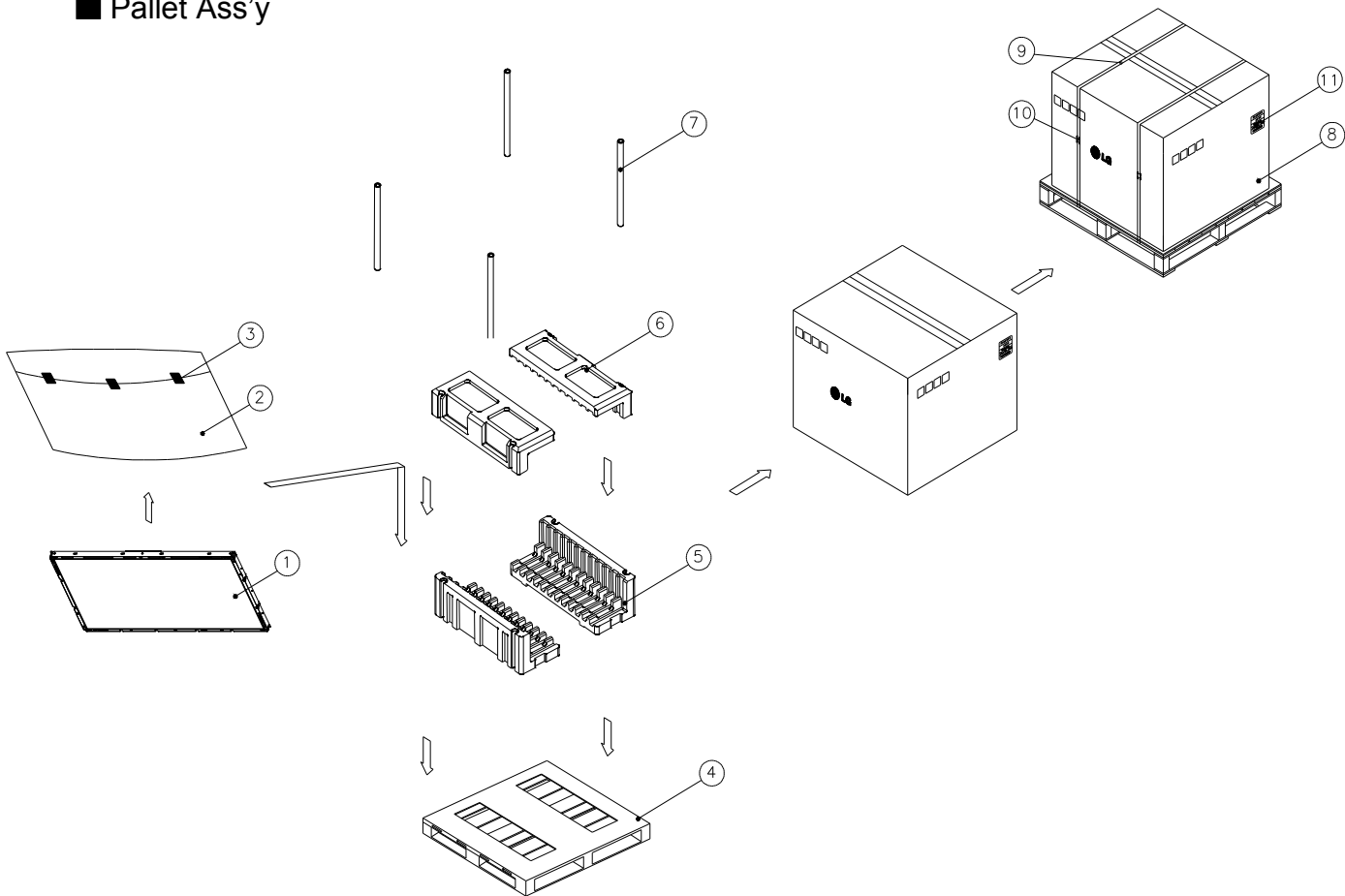
### 9-6. Handling Precautions for Protection Film

- (1) The protection film is attached to the bezel with a small masking tape.  
When the protection film is peeled off, static electricity is generated between the film and polarizer.  
This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

Product Specification

# APPENDIX-I

■ Pallet Ass'y



NO.	DESCRIPTION	MATERIAL
1	LCD Module	47" LCD
2	BAG	47INCH
3	TAPE	MASKING 20MM X 50M
4	PALLET	Plastic (---X---X---)
5	PACKING	EPS
6	PACKING	EPS
7	ANGLE POST	PAPER
8	ANGLE PACKING	PAPER
9	BAND,CLIP	STEEL
10	BAND	PP
11	LABEL	YUPO PAPER 80G 100X70

## # APPENDIX- II-1

## ■ LCM Label



## ■ Production site


- LG Display (Paju) Co., LTD
- LG Display (Guangzhou) Co., LTD

Note 1. The origin of LCM Label will be changed according to the production site.


Product Specification

# APPENDIX- II-2

Box Label

LC470DUE		
SFR1		
X pcs	001/01-01	
MADE IN KOREA		RoHS Verified
		

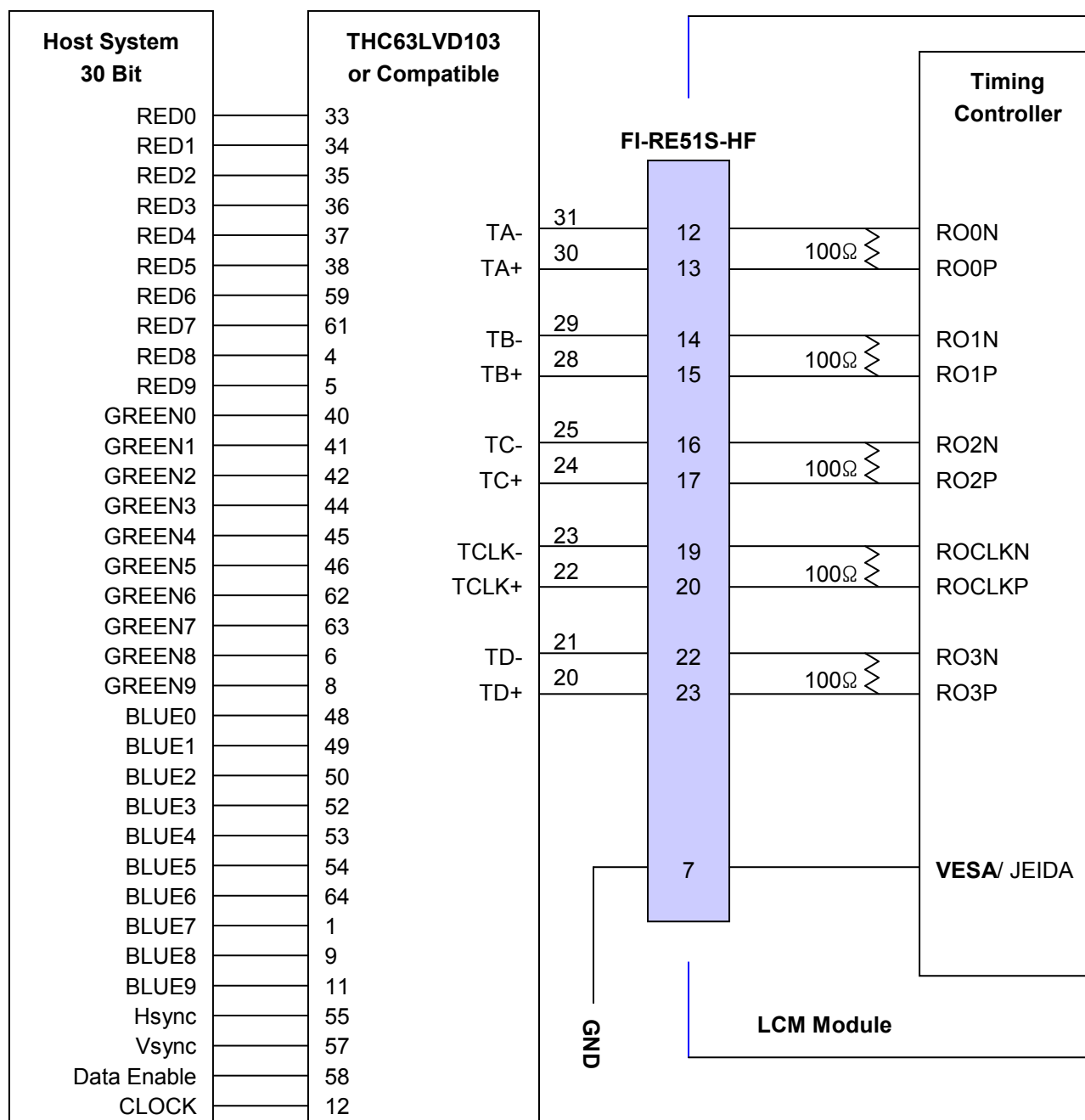
Pallet Label

100.0		
<div>LC470DUE</div> <div>SFR1</div>		
XX PCS	001/01-01	
MADE IN KOREA		RoHS Verified
<div></div> <div>XXXXXXXXXXXXXXXX XXX</div>		
		70.0

## Product Specification

## # APPENDIX- III-1

■ Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7= “L” or “NC”)



Note: 1. The LCD module uses a 100 Ohm[Ω] resistor between positive and negative lines of each receiver input.

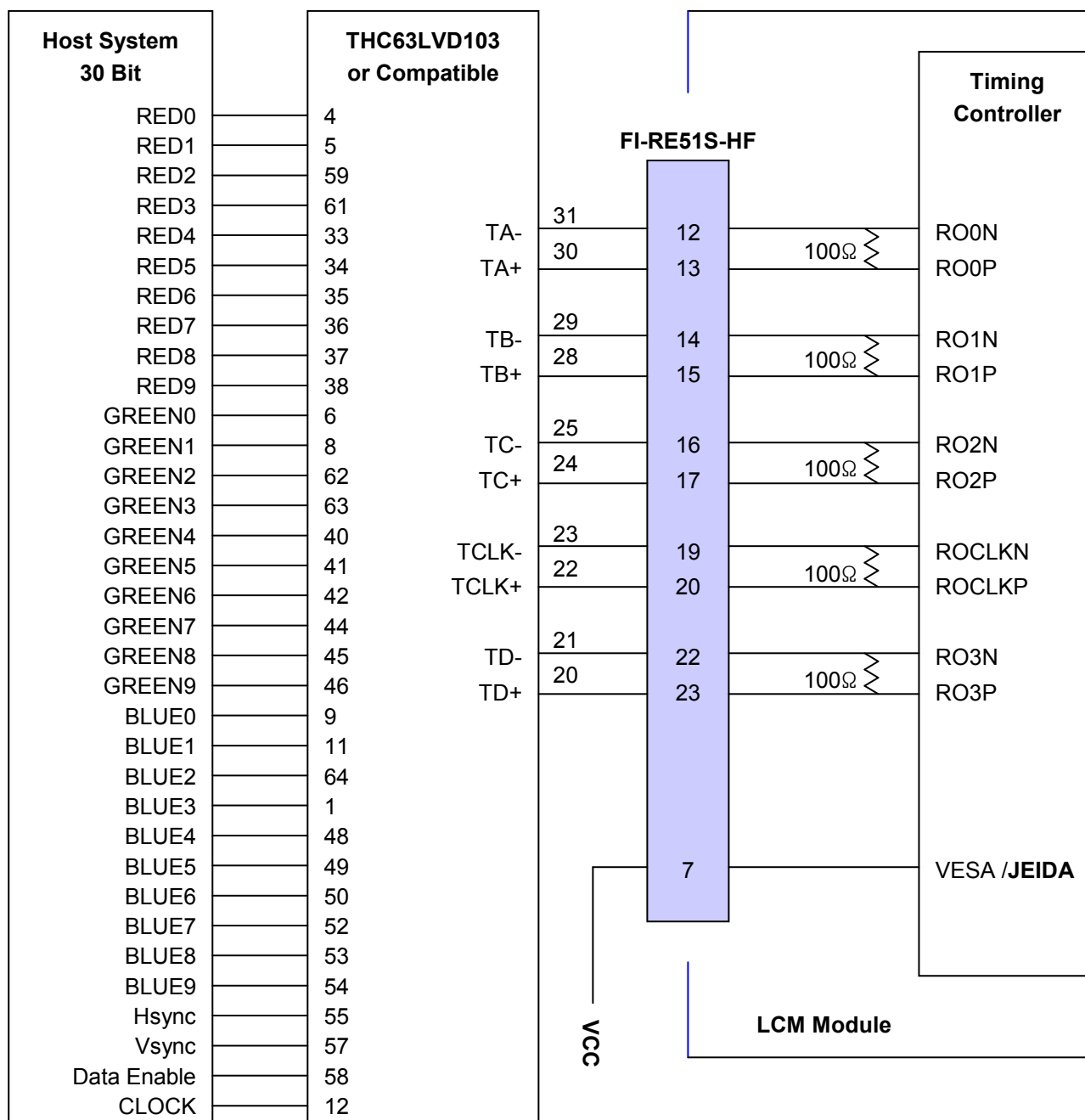
2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (THC63LVD103 or Compatible)

3. '7' means MSB and '0' means LSB at R,G,B pixel data.



## # APPENDIX- III-2

■ Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7= “H” )



Note :1. The LCD module uses a 100 Ohm[Ω] resistor between positive and negative lines of each receiver input.

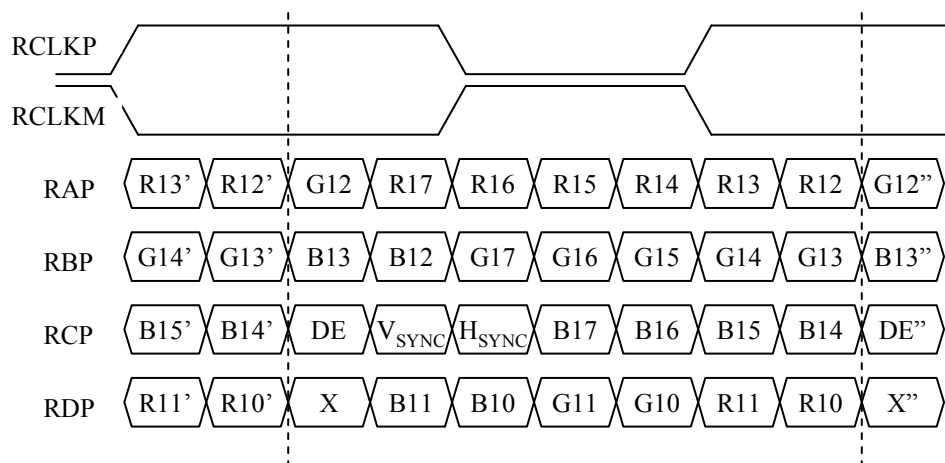
2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (THC63LVD103 or Compatible)

3. '7' means MSB and '0' means LSB at R,G,B pixel data.

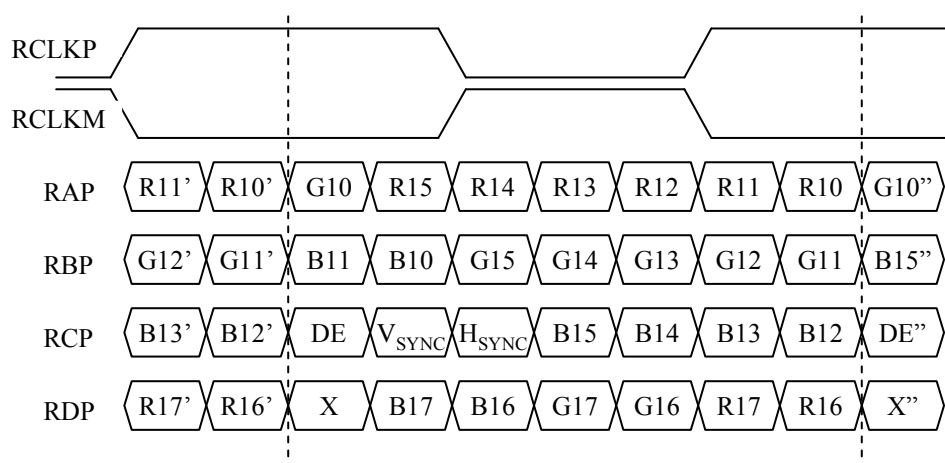
## # APPENDIX- IV

## ■ LVDS Data-Mapping Information (8 Bit )

## 1) LVDS Select : "H" Data-Mapping (JEIDA format)

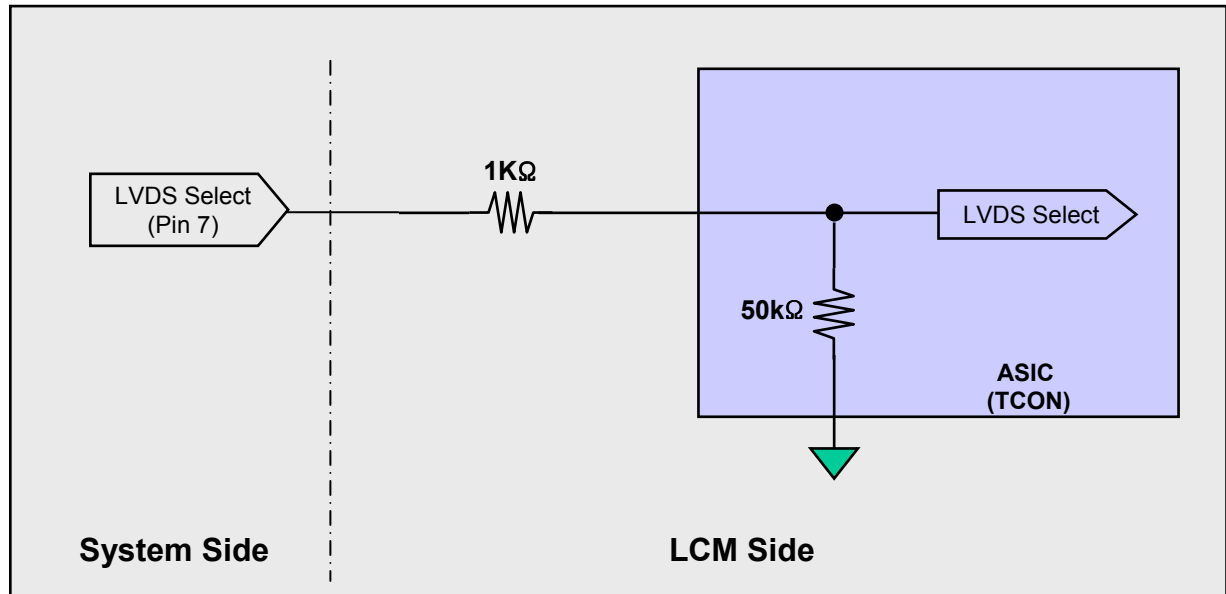


## 2) LVDS Select : "L" Data-Mapping (VESA format)



## # APPENDIX- V

## ■ Option Pin Circuit Block Diagram

1) Circuit Block Diagram of **LVDS Format** Selection pin

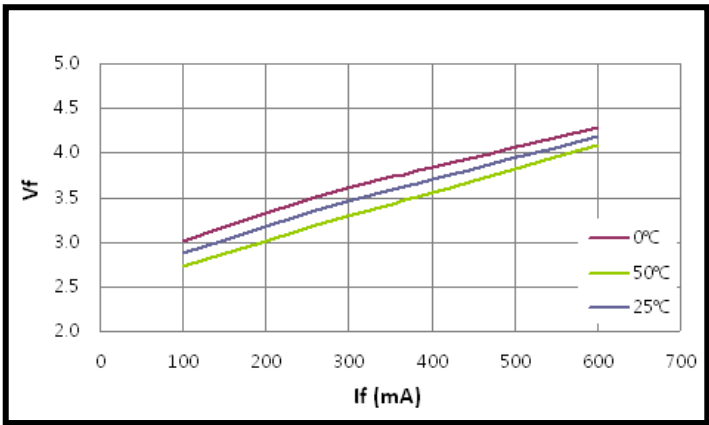
Product Specification

# APPENDIX- VII

LED Array Electrical Spec

Items	Symbol	Condition	Min	Typ	Max	Unit
Array Operating Voltage	Vf	IfM=400mA *1)	13.2	-	23.4	V

Forward Current vs. Forward Voltage



Ambient Temperature vs. Forward Voltage

